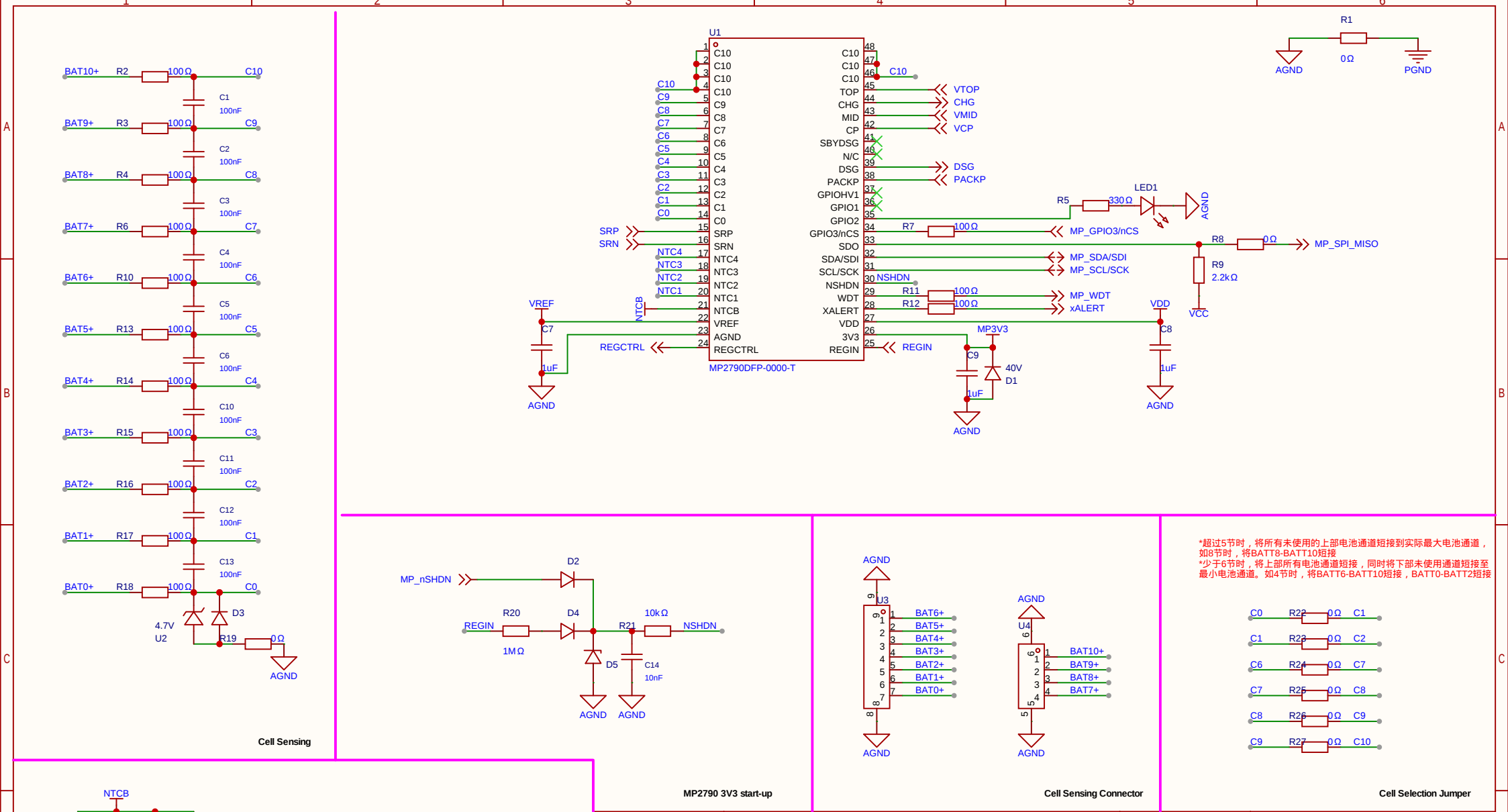
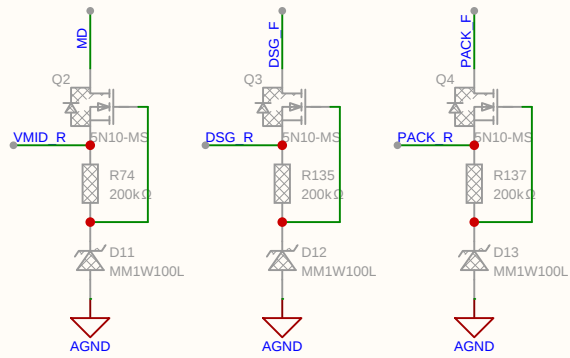


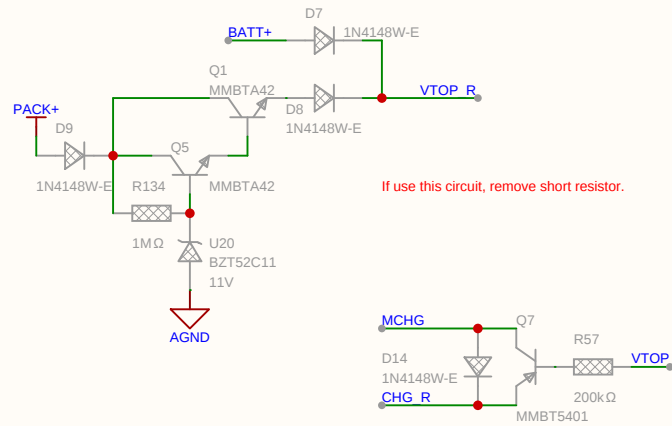


原理图		Schematic1		创建日期	2026-07-28
板子		Board1		更新日期	2026-08-12
绘制				图页	BMS AFE
审阅				4S1P_BMS_MPS	
		版本	尺寸	页 1 共 5	
嘉立创EDA		V1.0	A3	嘉立创EDA	

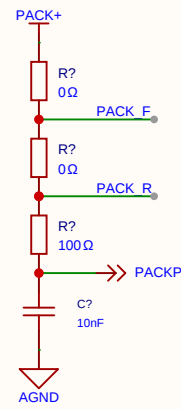
High voltage >75V charger connection. If use this circuit, remove short resistor.



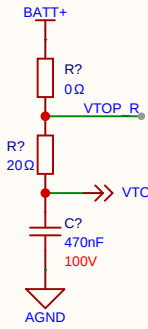
High-Voltage Charger Resistant Circuit(option)



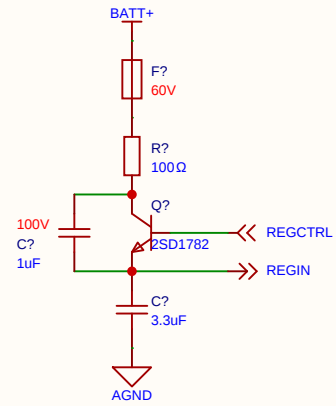
Vtop UVLO Power Supply by Charger Circuit(option)



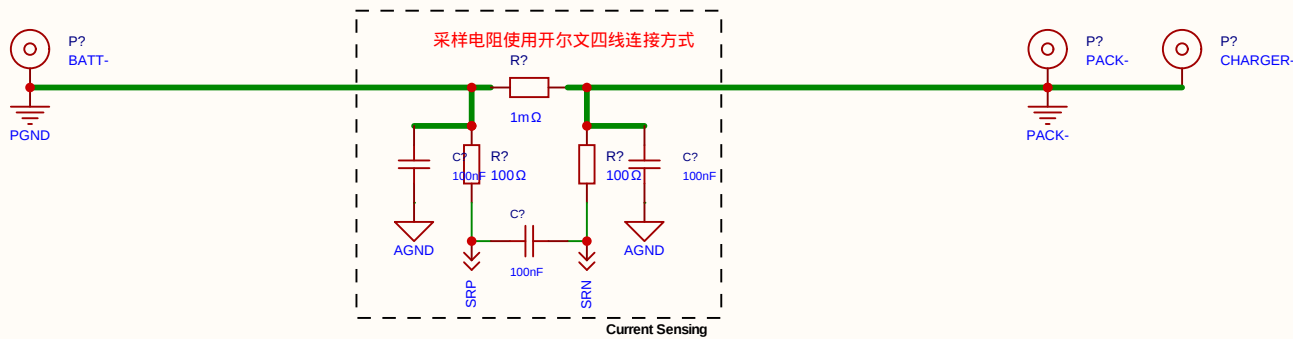
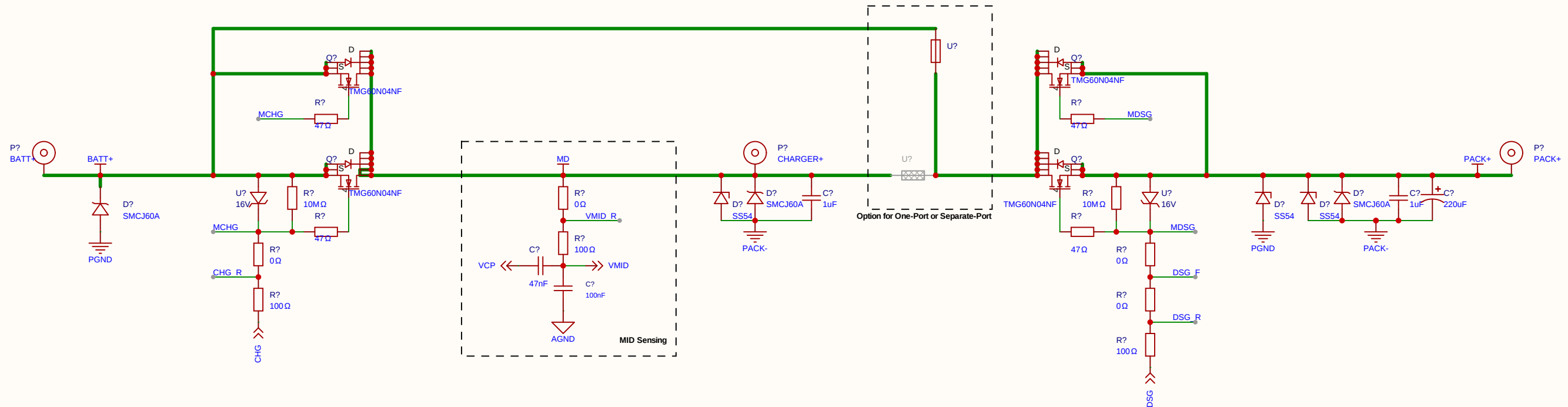
VPack Sensing



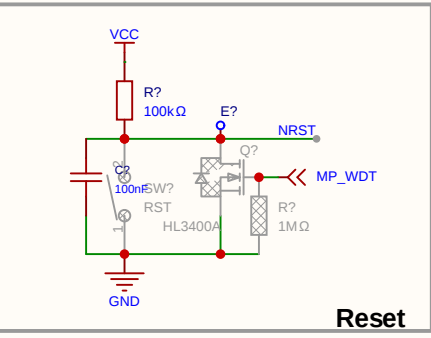
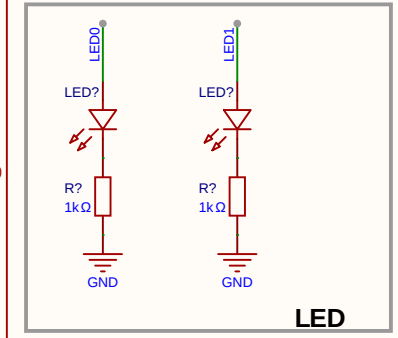
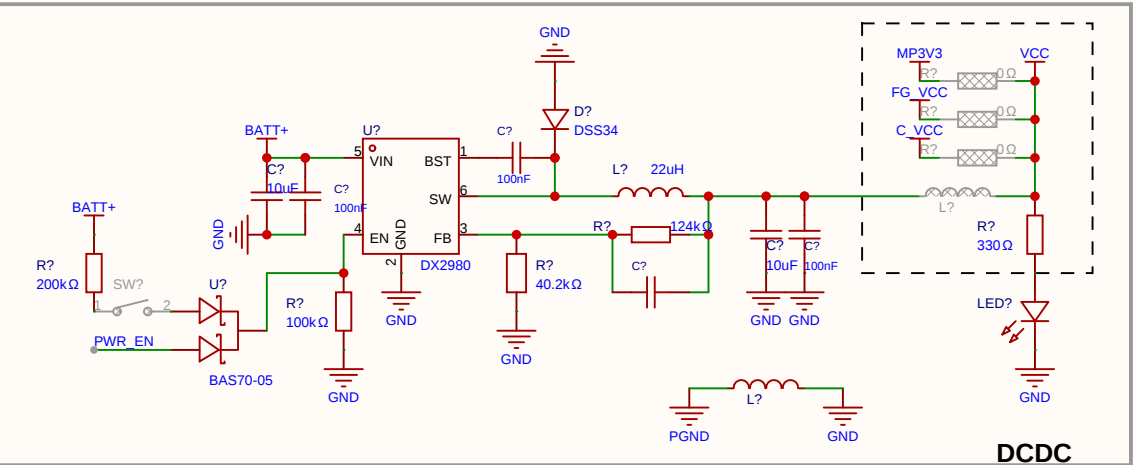
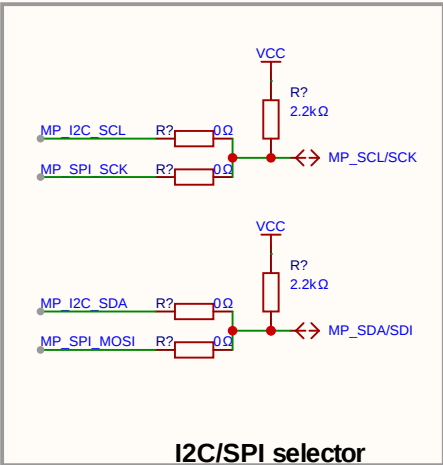
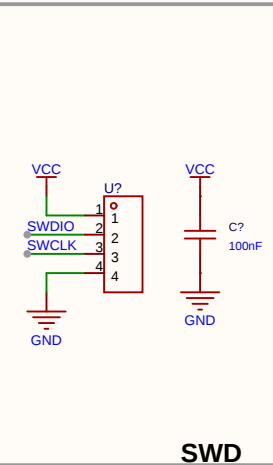
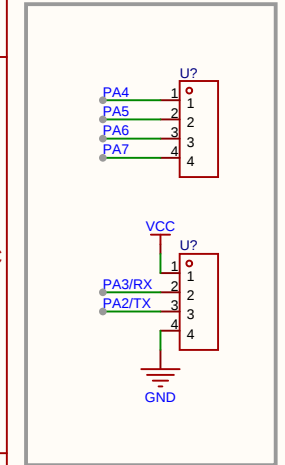
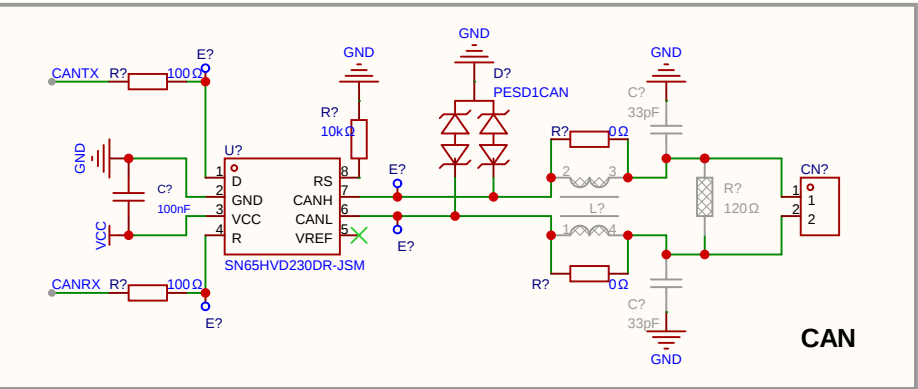
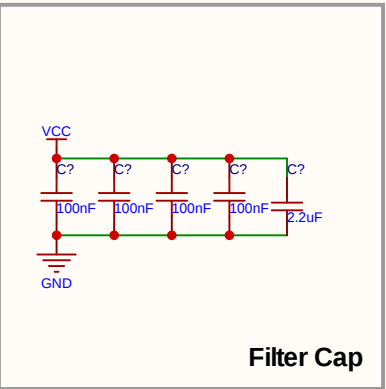
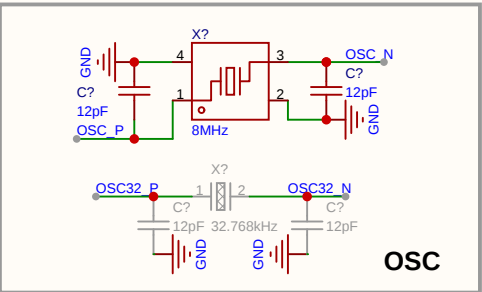
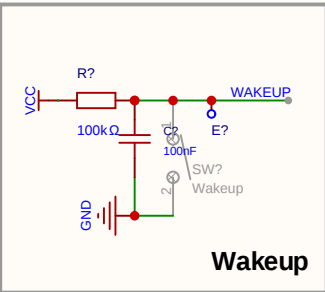
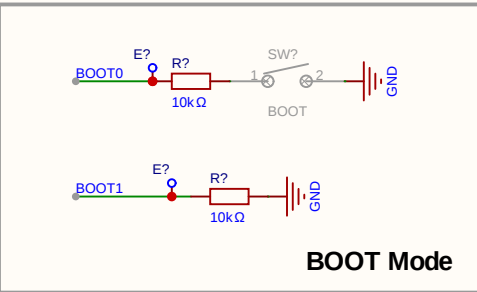
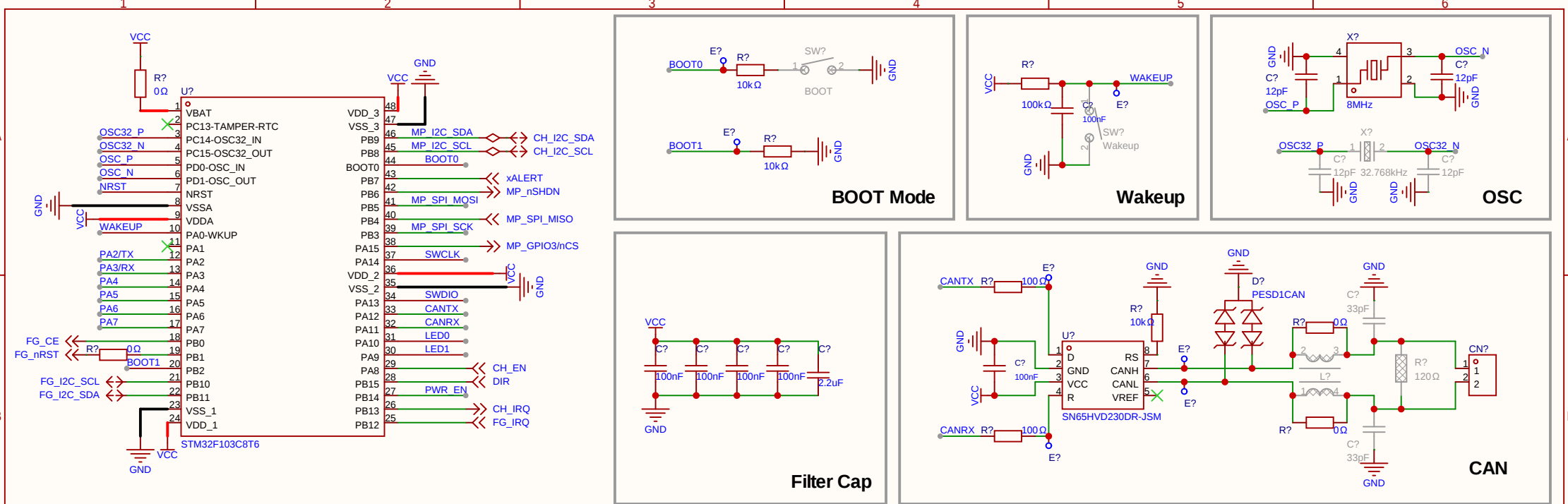
VBAT Sensing



External LDO

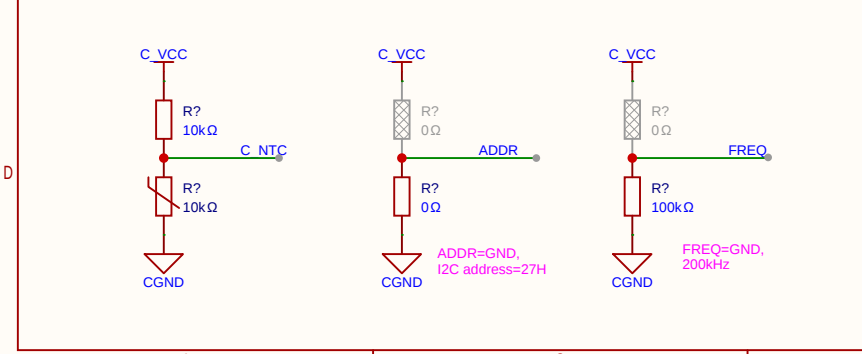
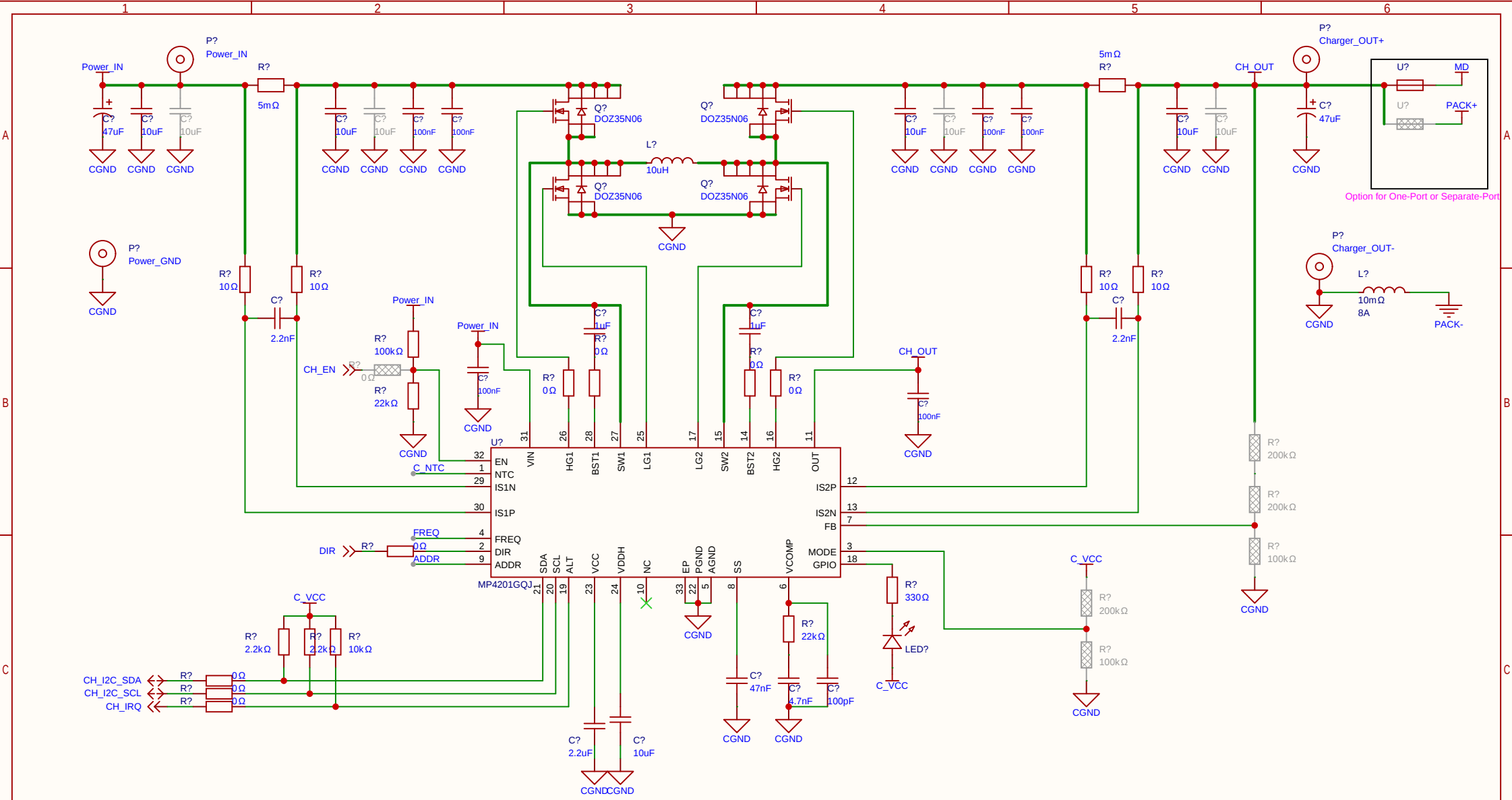


原理图	Schematic1			更新日期	2026-08-12
				创建日期	2026-08-02
图页	BMS Protections			物料编码	
绘制		4S1P_BMS_MPS			
审阅					
		版本	尺寸	页	3 共 5
		V1.0	A4	嘉立创EDA	



原理图	Schematic1		更新日期	2026-08-12
图页	Controller		创建日期	2026-08-02
绘制	4S1P_BMS_MPS		物料编码	
审阅				
版本		尺寸	页 4 共 5	
V1.0		A4	嘉立创EDA	





原理图	Schematic1			更新日期	2026-08-12
				创建日期	2026-08-04
图页	Charger			物料编码	
绘制		4S1P_BMS_MPS			
审阅					
		版本	尺寸	页	5 共 5
		V1.0	A4	嘉立创EDA	